EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	553	(fpga same (access visibility) same (dut microcontroller target))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/28 13:38
L2	422	(fpga same access same microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/28 13:39
L3	59	(fpga same interface) and (microcontroller same emulat$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/28 13:39
L4	13	(fpga same dut) and lock$step	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/28 13:39
L5	10	(in adj circuit adj emulator) and (fpga same pod)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/28 13:39
L6	82	(FPGA (virtual adj microcontroller) (virtual adj processor) (ICE) (emulator)) same ((I/O adj data) (IO adj data) (I-O adj data))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/28 13:40

5/28/08 4:34:28 PM
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